

August 12, 1999 (Case No.: RD-036)

Commissioner of Patents and Trademarks Washington, D.C. 20231

ar Sir:

Transmitted herewith for filing under 37 CFR § 1.53(b) is the application of:

Inventors:

Stefanos Sidiropoulos and Joe-Anand Louis-Chandran

For:

Integrated Circuit Device having I/O Structures with Reduced Input Loss

including:

[XX] Specification (30 consecutively numbered pages and 7 sheets of 1 set of formal drawings).

[XX] Declaration and Power of Attorney (2 pages).

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FEE CALCULATION

Basic Fee	\$ <u>760.00</u>
Additional Fees: Surcharge for more that 20 total claims	
Surcha	rge for more than 3 independent claims\$0-
Surcha	rge for multiple dependent claims\$0-
Total Filing Fee	
[]	A check payable to the Commissioner of Patents and Trademarks, in the amount of \$, is enclosed as payment for the above fees.
[XX]	Please charge my Deposit Account No. <u>50-0998</u> in the amount of <u>\$904.00</u> to cover the above fees. A duplicate copy of this sheet is enclosed.
[XX]	The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. <u>50-0998</u> . A duplicate copy of this sheet is enclosed.

Respectfully submitted

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